

CALL FOR SPECIAL SESSIONS

39th International Symposium on Multiple-Valued Logic

Naha Okinawa, Japan, May 21-23, 2009

The Multiple-Valued Logic Technical Committee of the IEEE Computer Society will hold its 39th annual symposium on May 21-23, 2009 in Okinawa, Japan. This symposium is sponsored by the IEEE Computer Society and Japan MVL Research Group. Special Sessions (SSs) are strongly encouraged. Each SS consists of at least five papers that provide a focused discussion of topic of interest. Each proposal must include an SS title, and a name, an affiliation, a country and an e-mail address of an SS organizer. Papers in SSs will undergo the same review process as regular conference submissions. We will help you solicit papers for your SS through the website. Please check website for submission process and other details. You are invited to submit proposals for special sessions on any subject in the area of multiple-valued logic, including but not limited to:

Algebraic and Formal Aspects,
Automatic Reasoning,
Logic Programming,
Philosophical Aspect,
Fuzzy Logic and Soft Computing,
Data Mining,
Switching Theory,

Test and Verification,
Spectral Techniques,
Circuit/Device Implementation,
VLSI Architecture,
VLSI Computing,
System-on-Chip Technology,
Nanotechnology.

Important Dates

- Deadline for Submission of Proposal for Special Sessions September 15, 2008
- Announcement of Call for Special Session Papers September 30, 2008
- Deadline for Submission of Special Session Papers November 1, 2008
(Same as general papers)

For special session proposal, please contact to

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