

*Begin End**Track1**Track2**May 21*

09:00	09:10	Opening	
09:10	09:50	Invited Talk 1	
09:50	10:00	10 min. Break	
10:00	12:05	Special Session 5(5)	Current-Mode Logic(5)
12:05	13:10	Lunch Break (Symposium Committee)	
13:10	14:50	Algebra(4)	Quantum Logic(4)
14:50	15:00	10 min. Break	
15:00	15:40	Invited Talk 2	
15:40	15:50	10 min. Break	
15:50	17:30	Special Session 2-1(3)	Logic Design and its Application(4)

May 22

09:30	10:10	Invited Talk 3	
10:10	10:20	10 min. Break	
10:20	12:00	Special Session 2-2(3)	Spectral Logic(4)
12:00	13:20	Lunch Break (ISMVL Executive Committee)	
13:20	14:20	Plenary Session	
14:20	17:30	Tour for Okinawa Traditions	
19:00	21:00	Banquet	

May 23

09:00	09:40	Invited Talk 4	
09:40	09:50	10 min. Break	
09:50	11:55	Special Session 3-1(5)	Multi-Valued VLSI(5)
11:55	13:00	Lunch Break	
13:00	13:40	Invited Talk 5	
13:40	13:50	10 min. Break	
13:50	15:30	Special Session 3-2(4)	Logic Design(4)
15:30	15:40	10 min. Break	
15:40	17:20	Emerging Device(4)	Decision Diagrams/ Reed-Muller Expansion(4)
17:20	17:30	Closing	

Special Session 2 : Clone Theory

Special Session 3 : Fuzzy and Rough Sets Theory, and Their Applications

Special Session 5 : Medical/Health Care Systems based on Soft Computing